

BUS
CONTROL

```
graph TD; BC[BUS CONTROL] --- Bus(( )); SLAVE1[SLAVE1] <--> Bus; SLAVE2[SLAVE2] <--> Bus; Master1[Master1] <--> Bus; Master2[Master2] <--> Bus; Master3[Master3] <--> Bus;
```

SLAVE1

Master1

Master2

SLAVE2

Master3