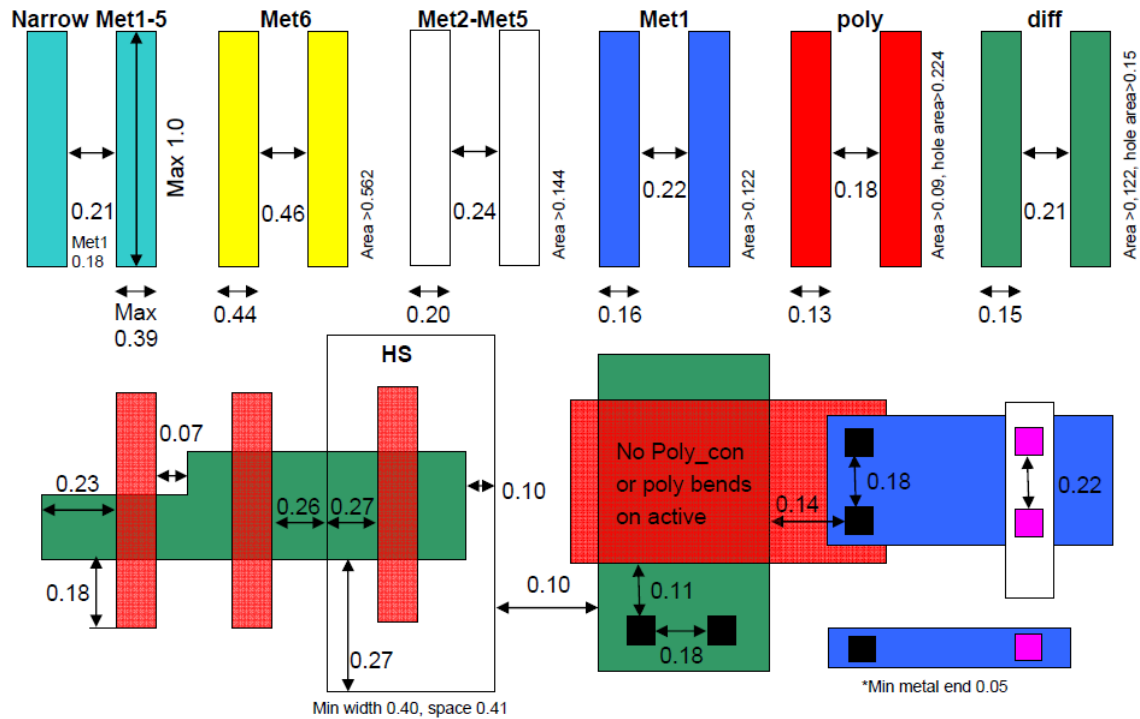


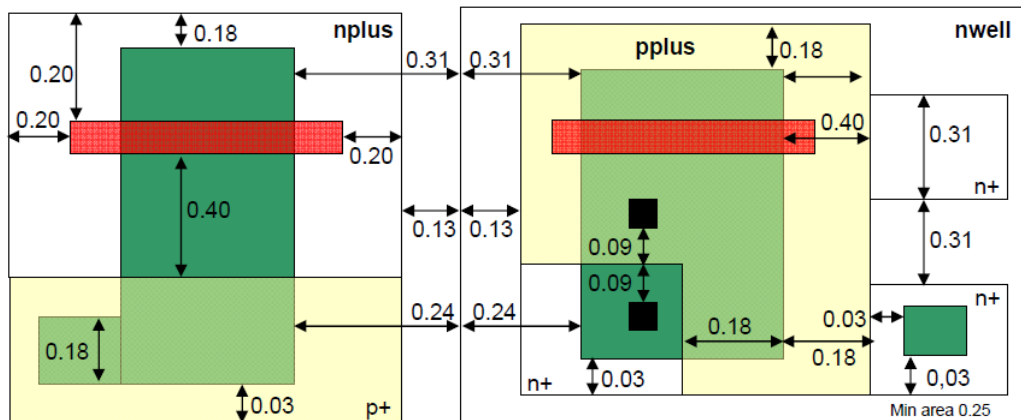
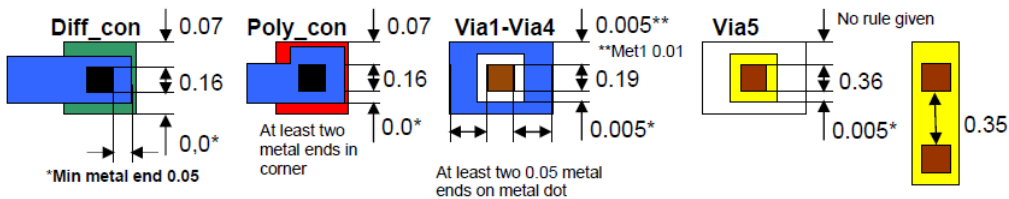
# Geometric design rules for 0.13 $\mu\text{m}$ CMOS



Contact size is 0.16\*0.16 exactly, minimum contact spacing is 0.18 (min 0.2 in array)

Via1-via4 size is 0.19\*0.19 exactly, minimum spacing is 0.22 (min 0.29 in array)

Via5 size is 0.36\*0.36 exactly, minimum spacing is 0.35 (min 0.54 in array)



Min well area 1.0, min well hole 0.384, min well width 0.62, min well space 0.62 (hot space 1.0)