

Term paper topic	Term paper group	Presentation time
Hardware/software codesign	Chhapanimohan, Snehal Manoj Xu, Ming	Tue Dec 11 9:30-9:50
Low-power digital circuit implementation	Panagiotou, Antonios Mathiesen, Christoffer Wang, Lin	Tue Dec 11 10:00-10:20
EDA for reconfigurable computing	Kotrappa, Prasanna Loganathan, Pradeep Gressenbuch, Luis	Tue Dec 11 10:25-10:45
Memory considerations for low power systems	Adler, Vincent Andersson, Elsa Tilly, Erik Lindh, Kristoffer	Tue Dec 11 11:00-11:20
EDA for signal and power integrity solutions	Jonsson Kulldorff, Alfred Croft, John	Tue Dec 11 11:25-11:45
Hardware/software codesign	Ruiz García, Miguel Speiser, Sebastian Han, Wenqian	Fri Dec 14 9:30-9:50
Adaptive circuits and systems	Ibrahimoglu Oscarsson, Yunus Zhu, Jiaying Ding, Sinan	Fri Dec 14 10:00-10:20
Design for test	Björkholm, Said Bryksa, Artsiom Gupta, Shilpa	Fri Dec 14 10:25-10:45
Scaling and design for manufacturability (DFM)	Ramachandran, Akshay Venkatasubramanian Raja Badarinath, Nakul Zhang, Yang	Fri Dec 14 11:00-11:20
SoC voltage regulation	Kvist, David Karlsson, Magnus Emanuelsson, Emil	Fri Dec 14 11:25-11:45