

ENM061 - Power Electronic Converters 7.5 ECTS, 2017

Mebtu Beza

mebtu.beza@chalmers.se

Chalmers University of Technology
Department of Electrical Engineering
Division of Electric Power Engineering

Lecture outline

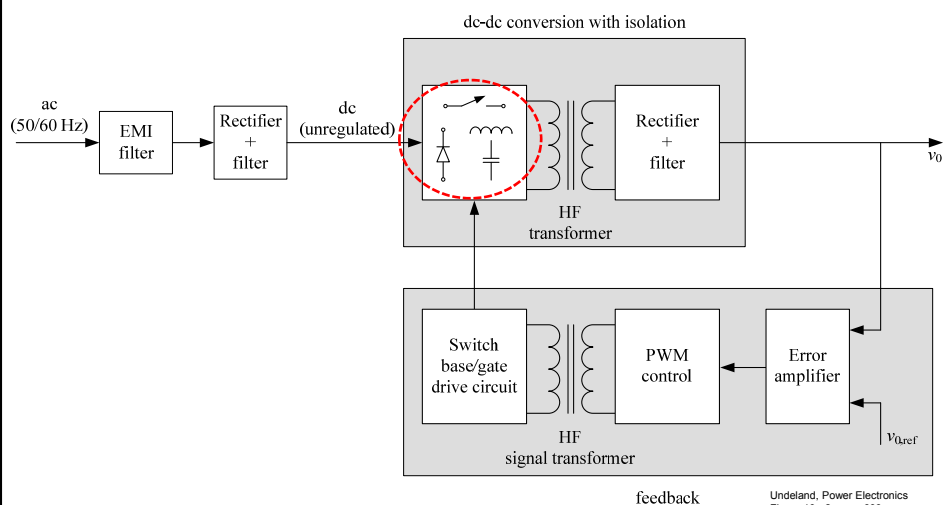
Step-up (Boost) and Buck-Boost DC/DC converters

- The Boost converter
 - ❖ Continuous conduction mode (CCM)
 - ❖ Discontinuous conduction mode (DCM)
 - ❖ Boundary conditions between CCM and DCM
 - ❖ Output voltage ripple
- The Buck-Boost converter
 - ❖ Continuous conduction mode (CCM)
 - ❖ Discontinuous conduction mode (DCM)
 - ❖ Boundary conditions between CCM and DCM
 - ❖ Output voltage ripple
- Brief description of the Tutorial and PSpice exercises
- Summary

Learning outcomes

- Fourier components and total harmonic distortion (THD) for basic waveforms.
- Operating principles of the most common active components (e.g. diode, thyristor, IGBT, and MOSFET) and passive components (e.g. capacitors, transformers and inductors).
- Operation of a pulse width modulation (PWM), the purpose of controlling the desired quantity and the need for a controller circuit within the power electronic converter.
- **Analysis of ideal DC/DC converters (e.g. buck, boost, buck-boost, flyback, the forward, the push-pull, half-bridge and full-bridge converters) in CCM and DCM operation.**
- Operating principles of single-phase and three-phase AC/DC inverters with different modulation strategies (e.g. PWM and square wave operation).
- Operation of multilevel converters (e.g. NPC, flying capacitor and MMC topologies) using current and voltage waveform analysis. Pros and Cons of the converter in terms of harmonics and losses.
- Operation of single- and three-phase diode rectifiers operating with voltage-stiff and current-stiff DC-side. Investigating the impact of line impedance within the converter circuit for current commutation.
- Operation of single- and three-phase thyristor rectifiers operating with a current-stiff DC-side and the impact of line impedance for current commutation. Investigating the use of 6/12-pulse configurations.
- Identify simple power electronic converter schematics. Recognizing the different parts in a physical circuit on which basic wave-shape and efficiency measurements is performed.
- Loss calculation in passive and active components. Evaluating the temperature rise in the active components and choosing an appropriate heat-sink. Gaining a basic understanding of component life time.
- Utilizing the software Cadence PSpice to simulate basic power electronic circuits and the practical labs to have a firsthand experience of how real DC/DC converters operate.

Switch-mode power supply



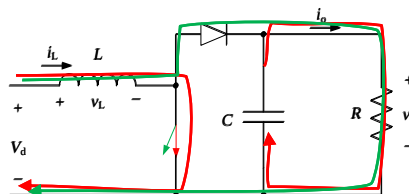
The Step-up DC/DC converter

Assumptions

- All active and passive components are ideal
- We have no losses (input power = output power)
- The source impedance is zero
- Converter is in steady-state condition and the
 - ❖ average inductor voltage is zero
 - ❖ average capacitor current is zero
- Very large output capacitor => constant output voltage
- Non-ideal behaviors such as losses and voltage ripple are considered after derivations using the ideal assumptions

The Boost Converter - CCM

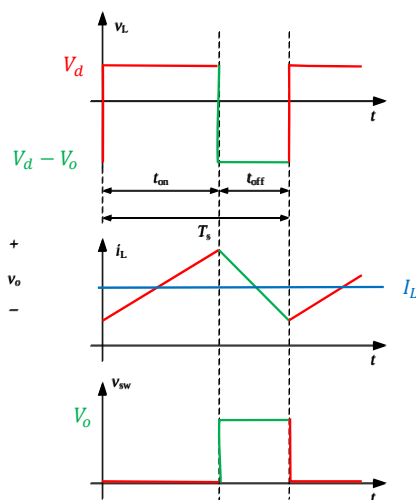
Ex.: *plot curves*



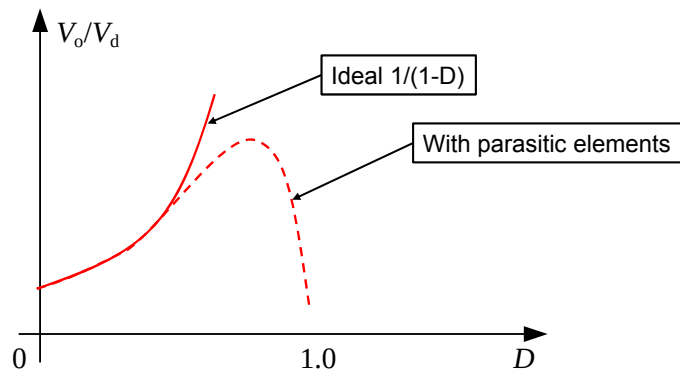
$$\frac{V_o}{V_d} = \frac{1}{1-D}$$

$$I_o = (1-D)I_L$$

$$i_C = i_D - I_o$$

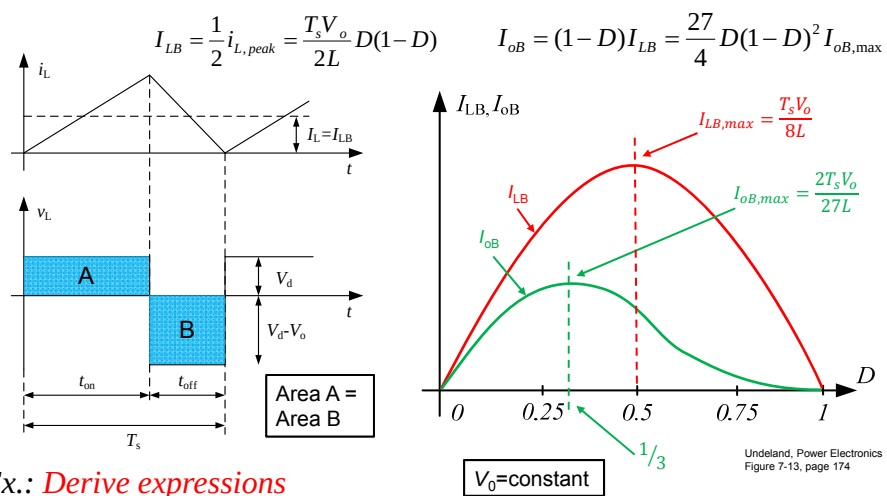


The Boost Converter Ideal and Actual Duty Ratio



Undeland, Power Electronics
Figure 7-16, page 177

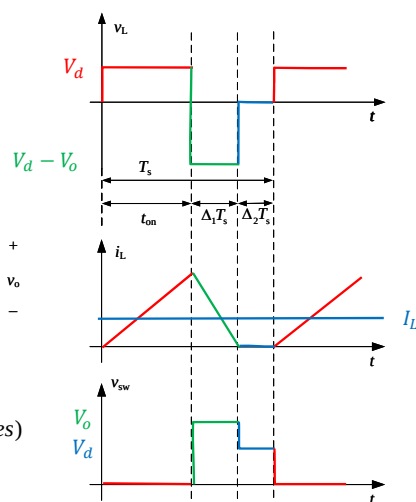
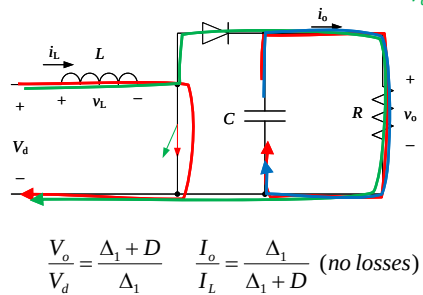
The Boost Converter – Boundary Between CCM and DCM



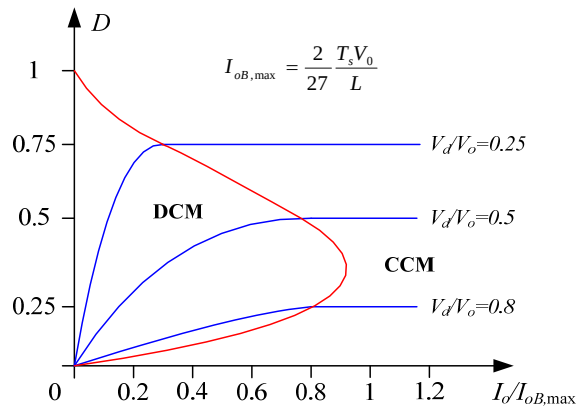
Undeland, Power Electronics
Figure 7-13, page 174

The Boost Converter - DCM

Ex.: *plot curves*



The Boost Converter – Boundary Between CCM/DCM with Constant V_o



- Boundary

$$I_{oB} = \frac{27}{4} D(1-D)^2 I_{oB,max}$$

- CCM

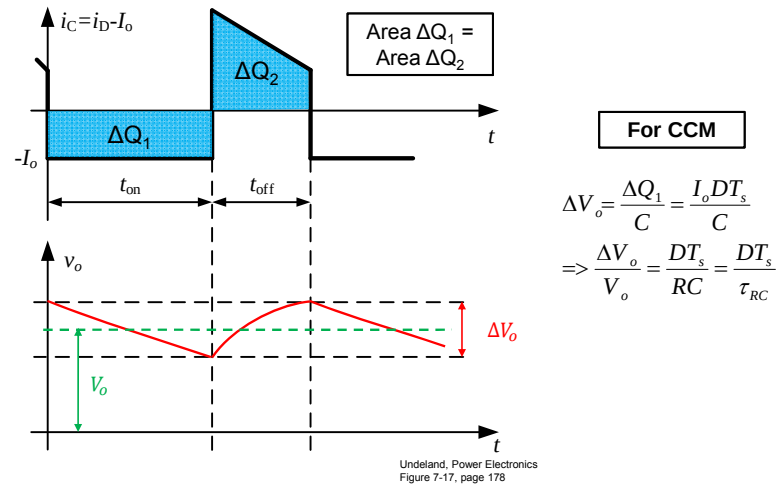
$$\frac{V_o}{V_d} = \frac{1}{1-D}$$

- DCM

$$D = \sqrt{\frac{4}{27} \frac{V_o}{V_d} \left(\frac{V_o}{V_d} - 1 \right) \frac{I_o}{I_{oB,max}}}$$

Ex.: *Derive expressions*

The Boost Converter Output Voltage Ripple

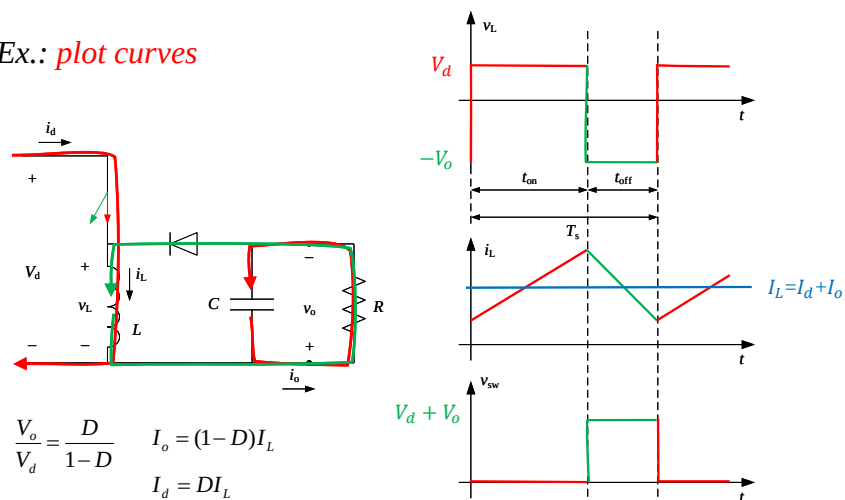


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The Buck-Boost Converter - CCM

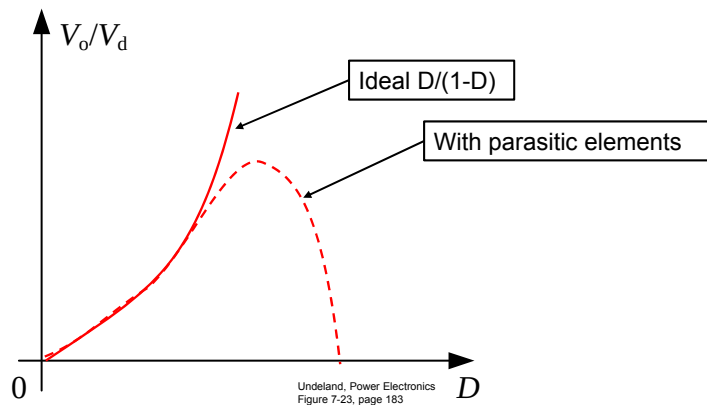
Ex.: plot curves



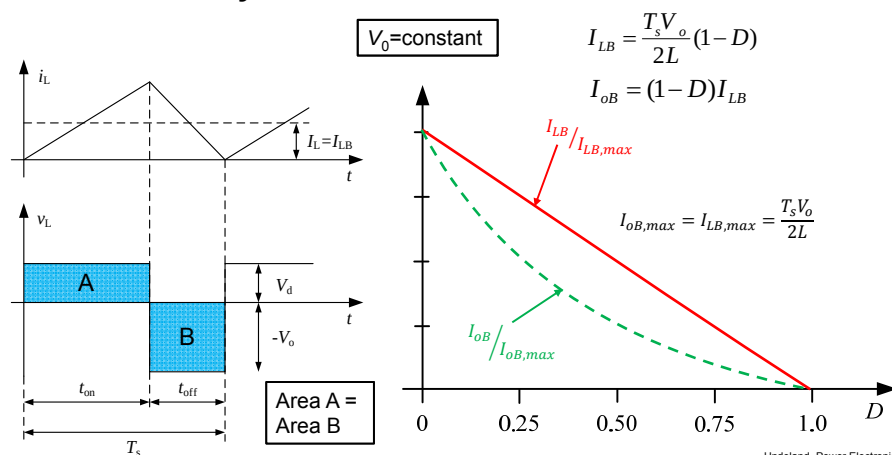
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The Buck-Boost Converter Ideal and Actual Duty Ratio



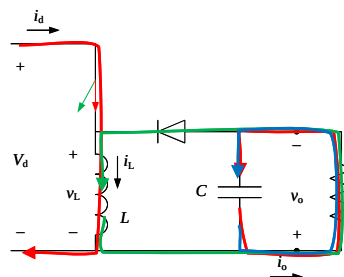
The Buck-Boost Converter Boundary Between CCM and DCM



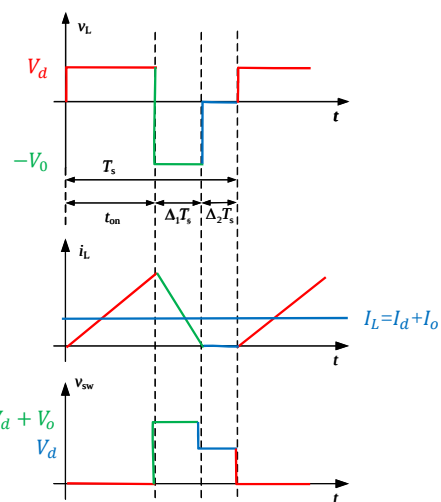
Ex.: *Derive expressions*

The Buck-Boost Converter - DCM

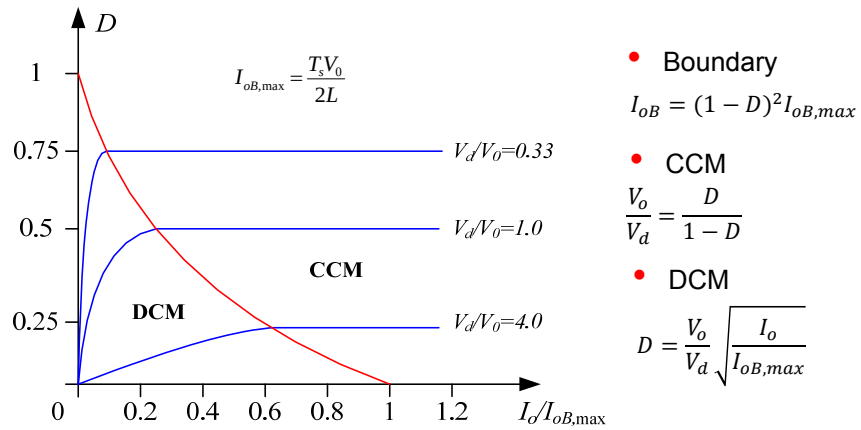
Ex.: *plot curves*



$$\frac{V_o}{V_d} = \frac{D}{\Delta_1} \quad \frac{I_o}{I_d} = \frac{\Delta_1}{D} \quad (\text{no losses})$$

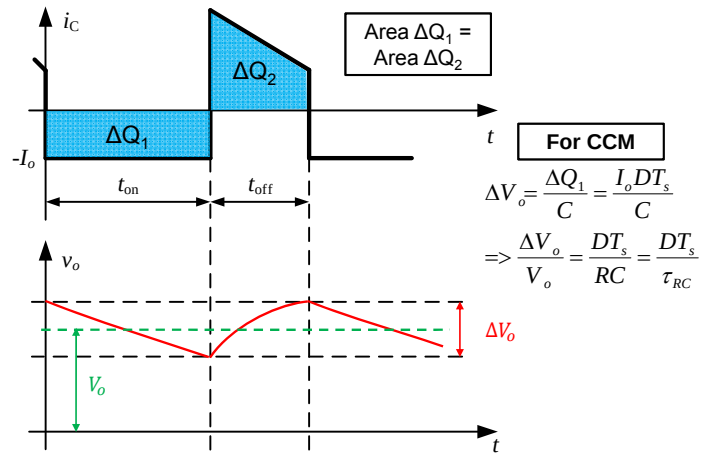


The Buck-Boost Converter – Boundary Between CCM/DCM with Constant V_o



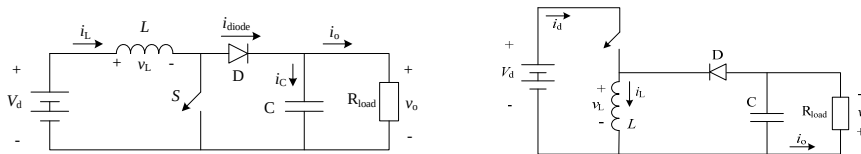
Ex.: *Derive expressions*

The Buck-Boost Converter Output Voltage Ripple



Undeland, Power Electronics
Figure 7-24, page 184

Tutorial 4 and 5



$V_o = 24\text{V}$ (constant), $V_d = 8 - 16\text{V}$, $P_o > 5\text{W}$, $C = 470\text{ }\mu\text{F}$ and $f_{sw} = 20\text{ kHz}$

- L_{min} for CCM mode of operation in the entire operating range

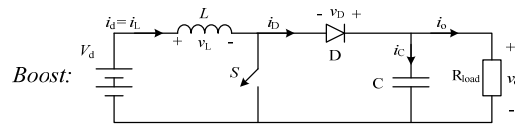
$V_o = 24\text{V}$, $V_d = 12\text{V}$, $I_o = 0.5\text{A}$, $L = 150\text{ }\mu\text{H}$, $C = 470\text{ }\mu\text{F}$ and $f_{sw} = 20\text{ kHz}$

- Voltage ripple (First CCM or DCM?)

$V_o = 15\text{V}$ (Buck-Boost converter above), $V_d = 12\text{V}$, $I_o = 2.5\text{A}$, L , C and f_{sw} given:

- Voltage ripple (First CCM or DCM?)

PSpice 2



- Waveforms, CCM or DCM, average and ripple output voltage
- FFT of the output voltage
- Impact of L , C , R_{load} , and R_{ESR}
- Impact of increasing D with parasitic components

Summary

- Describe the components of boost and buck-boost converters
- Describe how to identify the modes of operation of boost and buck-boost converters in CCM, DCM and Boundary conditions
- Show how to calculate the voltage ripple in the two converters
- Learning outcome:
 - ❖ Analysis of boost and buck-boost DC/DC converters in CCM and DCM operation.