

Examination**ENM060 Power Electronic Converters****Date and time** Tuesday April 14th, 2015, 14:00 – 18:00**Responsible Teacher:** Andreas Karvonen, tel. 0709-524924**Authorised Aids:** Chalmers-approved calculator (Casio FX82..., Texas Instruments Ti30... and Sharp ELW531...)**Grades:** U, 3, 4 or 5. (The limit for a 3 on the exam is 20p, a 4 is 30p and 5 is 40p. The maximum number of points is 50.)**Solutions:** Course webpage (Ping-Pong), April 15th 2015**Review of Exam** May 11th and May 13th, 12:00-13:00.
Fredrik Lamms Room. Division of Electric Power Engineering (1st floor).From May 15th 2015, the exams can be picked-up at the exam office, Department of Energy and Environment.

Location: EDIT building, Maskingränd 2, 3Ö (east) floor, room 3434A.

Opening hours during semesters: Monday-Friday 12:30-14:30

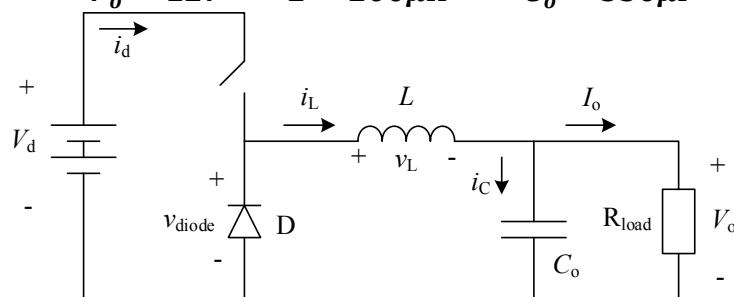
Observe that the questions are not arranged in any kind of order.

On the last pages there are some formulas that can be used in the examination. Always assume steady-state conditions in all tasks unless otherwise stated.

Please, read through the exam before you start.

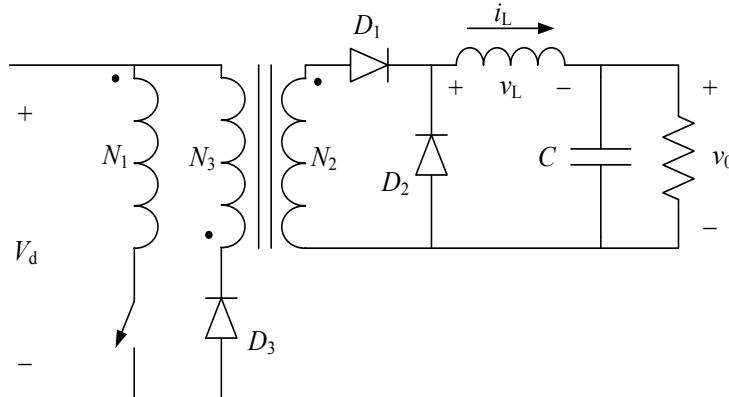
1) Consider the buck converter below. Calculate the lowest switching frequency that can be used if the converter always shall operate in CCM. (4p)

$$30V \leq V_d \leq 40V \quad V_o = 12V \quad L = 200\mu H \quad C_o = 330\mu F \quad 0.5A \leq I_o \leq 7A$$



2) A transformer is added to the buck converter in (1) so that it becomes a Forward converter. Name two reasons for having a galvanically isolated output from your DC/DC converter. (2p)

3) The forward converter below is operated with a varying output voltage. Select suitable values for the output inductance (L_{out}) and the output capacitance (C_{out}) so that converter meets the specifications of current (Δi_L) and voltage (Δv_o) ripple. Assume that $N_1 = N_3$, $N_2/N_1 = 0.36$ and that the converter shall always operate in CCM. (5p)



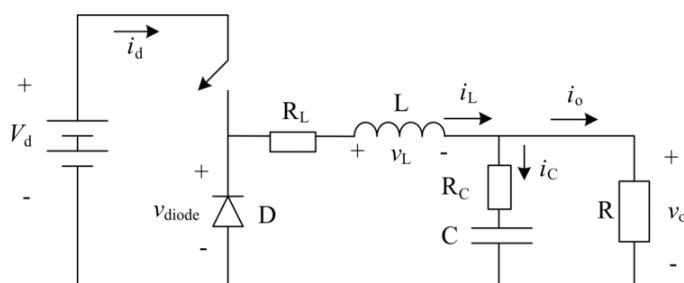
$$360V \leq V_d \leq 400V \quad V_o = 54V \quad f_{sw} = 100kHz \quad P_o = 500W$$

$\Delta i_{L(peak-peak)} \leq 20\%$ of the load current

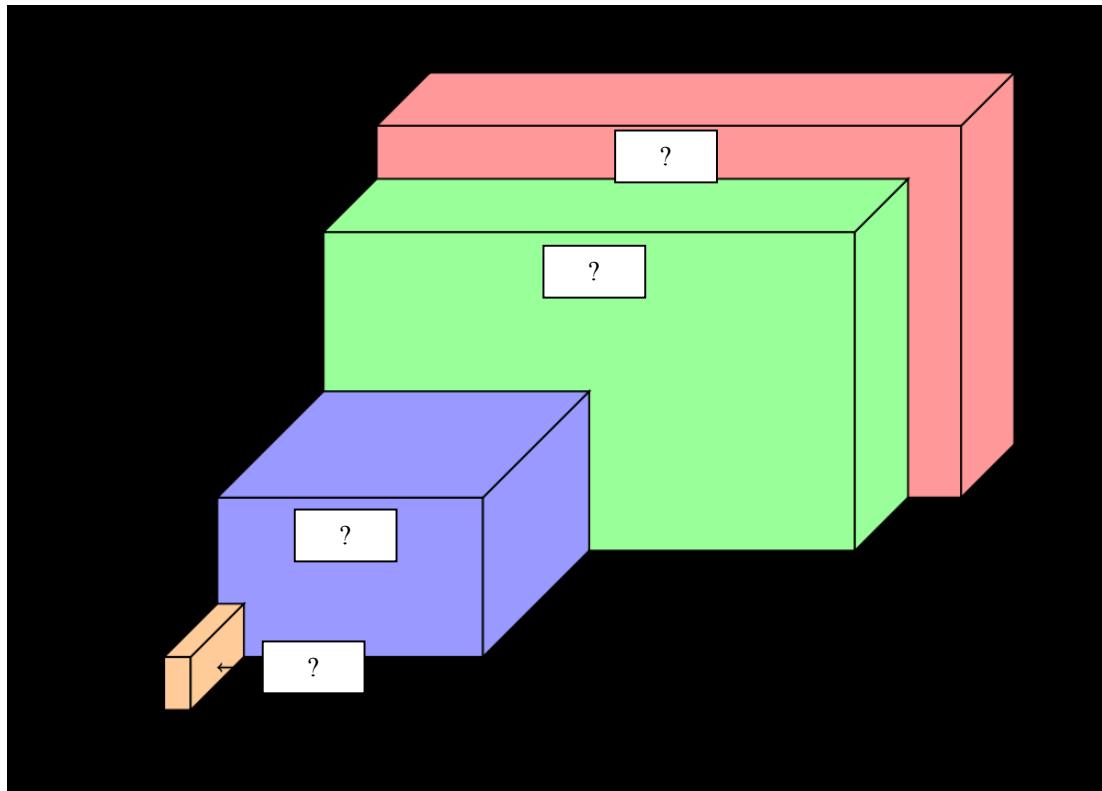
$\Delta v_{o(peak-peak)} \leq 20mV$

4) For the forward converter in (3), a new transformer shall be designed. You have two different E13/6/3-cores made of material 3C90 to choose between; a core with 0.25mm airgap and a core without airgap (see attached datasheet). Calculate the primary magnetizing inductance for both cores if $N_1 = 27$ with both the A_L -value and the reluctance of the core. Which core is most suitable in a forward transformer? Why? (3p)

5) The buck converter below is used with real components (parasitic elements are included) and a controller circuit that keeps the output voltage constant. Explain thoroughly what happens if a load step (e.g. sudden increase/decrease of the output current) is applied on the output. Also, explain the effect of the parasitic resistances (R_L and R_C) on the transfer function of the power stage ($T_p(s)$). (4p)



6) In the question marks in graph below, name which component that is suitable for each corresponding power level (4p)



7) A single phase inverter is operating in square wave mode with a load that consists of an inductor ($L = 100mH$) in series with a sinusoidally shaped back-emf voltage source ($e_o = \sqrt{2} \cdot E_o \cdot \sin(\omega_1 t)$). The output fundamental voltage ($V_{o(1)}$) has a frequency of 50Hz and the same amplitude and phase as the back-emf. The peak ripple in the output current is 2A, shown in the figure below. Calculate the resulting DC-link voltage (V_d). (4p)

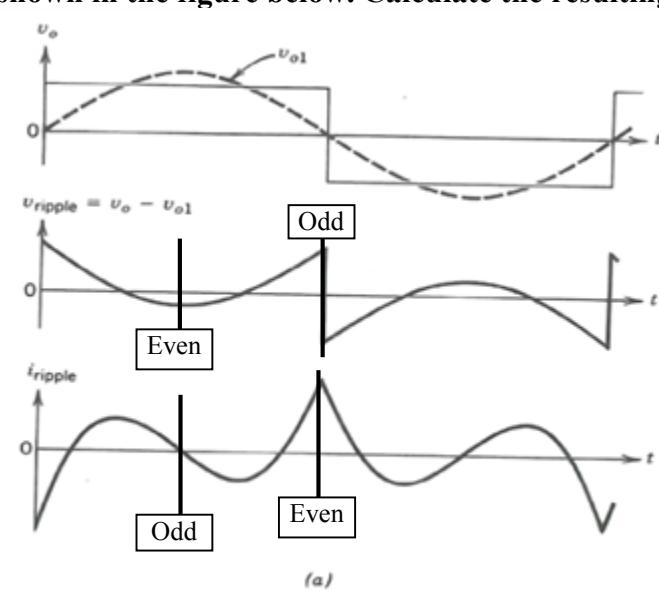
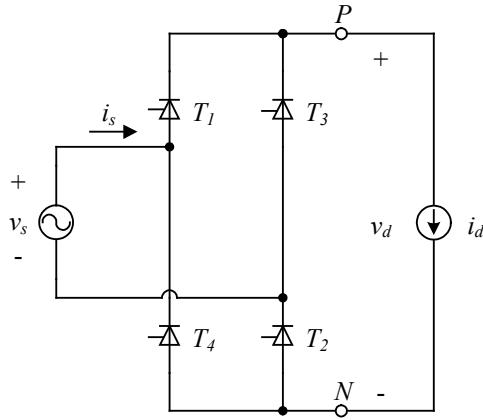


Figure 8-19 Ripple in the inverter output: (a) square-wave

8) Consider a single phase inverter operating in PWM mode where the output current is sinusoidal and lagging the voltage. Why is blanking time needed? How will the blanking time affect the output current and voltage? Explain by e.g. drawings and harmonic content. (4p)

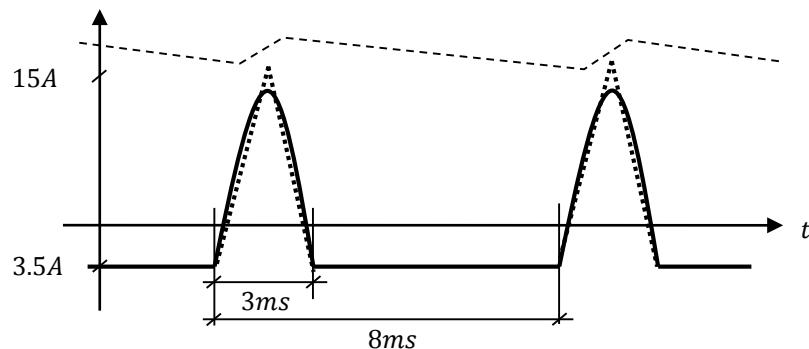
9) Consider the single phase thyristor rectifier below.



The input voltage (v_s) is a sinusoidal with an amplitude of 200V and a frequency of 50Hz. The delay angle (α) is 60° and the DC-side current (I_d) is constant current of 10A. Draw the output voltage waveform (v_d) and calculate the average value V_d . (3p)

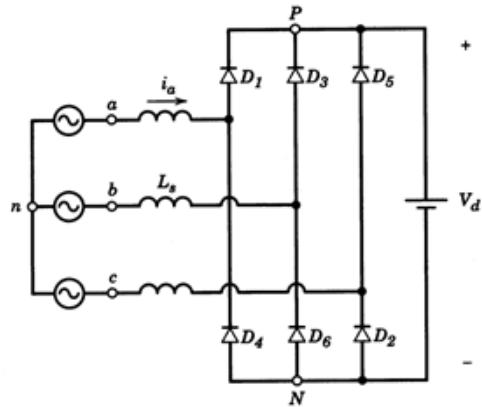
10) The single phase thyristor rectifier in (9) is now fed with an input voltage that is square wave shaped with an amplitude of 200VRMS and a frequency of 50Hz. If the source inductance (L_s) is considered, draw the output voltage (v_d), the voltage over the source inductance (L_s) and the source current (i_s) for the delay angle $\alpha = 30^\circ$. (4p)

11) A single phase diode rectifier is used with a voltage stiff DC-side. The output voltage (dashed line, not to scale) and the capacitor current are depicted below.

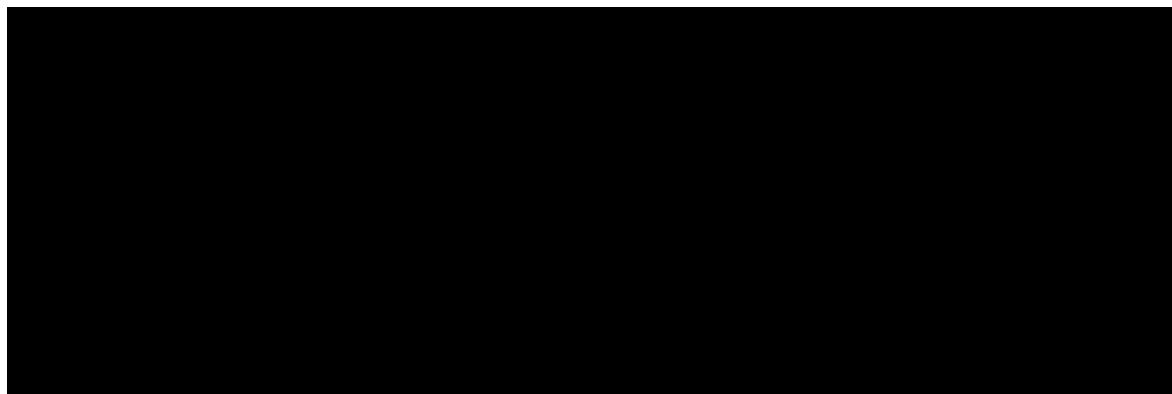


The capacitor current is sinusoidal shaped during 3ms. For reasons of simplicity, the current can be approximated with a triangular shape (see dotted curve) instead. Based on the two attached datasheets, select a suitable diode and calculate the resulting component temperature. Assume that the ambient temperature is $60^\circ C$. (5p)

12) The three phase diode rectifier below is used with a voltage stiff DC-link and a negligible source inductance. The system operates with 50Hz and $v_a = v_b = v_c = 230V$ peak voltage. Draw the phase voltages (v_a, v_b, v_c), the resulting line-to-line voltage between phase a and b (v_{ab}) and the line current i_a . Clearly state the amplitudes and phase shifts between the voltages. The exact values of the line current are not needed. (4p)



13) For a three phase inverter operating in PWM-mode, sketch the resulting harmonic spectrum for $m_a = 0.8$ and $m_f = 15$. Mark the amplitudes for the specified frequencies (and sidebands) in the diagram. (4p)



Formulas for Examination in Power Electronic Converters (ENM060)

Table 3-1 Use of Symmetry in Fourier Analysis

Symmetry	Condition Required	a_h and b_h
Even	$f(-t) = f(t)$	$b_h = 0$ $a_h = \frac{2}{\pi} \int_0^\pi f(t) \cos(h\omega t) d(\omega t)$
Odd	$f(-t) = -f(t)$	$a_h = 0$ $b_h = \frac{2}{\pi} \int_0^\pi f(t) \sin(h\omega t) d(\omega t)$
Half-wave	$f(t) = -f(t + \frac{1}{2}T)$	$a_h = b_h = 0$ for even h $a_h = \frac{2}{\pi} \int_0^\pi f(t) \cos(h\omega t) d(\omega t)$ for odd h $b_h = \frac{2}{\pi} \int_0^\pi f(t) \sin(h\omega t) d(\omega t)$ for odd h
Even quarter-wave	Even and half-wave	$b_h = 0$ for all h $a_h = \begin{cases} \frac{4}{\pi} \int_0^{\pi/2} f(t) \cos(h\omega t) d(\omega t) & \text{for odd } h \\ 0 & \text{for even } h \end{cases}$
Odd quarter-wave	Odd and half-wave	$a_h = 0$ for all h $b_h = \begin{cases} \frac{4}{\pi} \int_0^{\pi/2} f(t) \sin(h\omega t) d(\omega t) & \text{for odd } h \\ 0 & \text{for even } h \end{cases}$

Definition of RMS-value:

$$F_{RMS} = \sqrt{\frac{1}{T} \int_{t_0}^{t_0+T} f(t)^2 dt}$$

Definition of RMS-value with Fourier-series:

$$F_{RMS} = \sqrt{F_0^2 + \sum_{n=1}^{\infty} F_n^2} = \sqrt{\left(\frac{a_0}{2}\right)^2 + \sum_{n=1}^{\infty} \left(\frac{\sqrt{a_n^2 + b_n^2}}{\sqrt{2}}\right)^2}$$

$$\sin^2(\alpha) + \cos^2(\alpha) = 1$$

$$\sin(\alpha + \beta) = \sin(\alpha)\cos(\beta) + \cos(\alpha)\sin(\beta)$$

$$\cos(\alpha + \beta) = \cos(\alpha)\cos(\beta) - \sin(\alpha)\sin(\beta)$$

$$\sin(\alpha)\sin(\beta) = \frac{1}{2}(\cos(\alpha - \beta) - \cos(\alpha + \beta))$$

$$\cos(\alpha)\cos(\beta) = \frac{1}{2}(\cos(\alpha - \beta) + \cos(\alpha + \beta))$$

$$\int \sin(ax) dx = -\frac{1}{a} \cos(ax), \int x \sin(ax) dx = \frac{1}{a^2} (\sin(ax) - ax \cos(ax)), \int \cos(ax) dx = \frac{1}{a} \sin(ax)$$

$$\int x \cos(ax) dx = \frac{1}{a^2} (\cos(ax) + ax \sin(ax))$$

$$PF = \frac{P}{S} = \frac{V_s I_{s1} \cos \phi_1}{V_s I_s}, DPF = \cos \phi_1, \%THD_i = 100 \frac{I_{dis}}{I_{s1}} = 100 \frac{\sqrt{I_s^2 - I_{s1}^2}}{I_{s1}} = 100 \sqrt{\sum_{h \neq 1} \left(\frac{I_{sh}}{I_{s1}} \right)^2}$$

Electromagnetics

$$e = \frac{d}{dt} \psi \quad \psi = N\phi \quad \phi = BA \quad R = \frac{l}{A\mu_r\mu_0} \quad L = \frac{\Psi}{i}$$

$$NI = R\phi = mmf \quad N\phi = LI \quad L = A_L N^2 \quad W = \frac{1}{2} LI^2$$

Simpson's rule

Let $f(x)$ be a polynomial of maximum third degree, this means

$$f(x) = a_1 + a_2 x + a_3 x^2 + a_4 x^3$$

For this function the integral can be calculated as

$$\frac{1}{T} \int_{t_0}^{t_0+T} f(x) dx = \frac{1}{6} \left(f(t_0) + 4f(t_0 + \frac{T}{2}) + f(t_0 + T) \right)$$

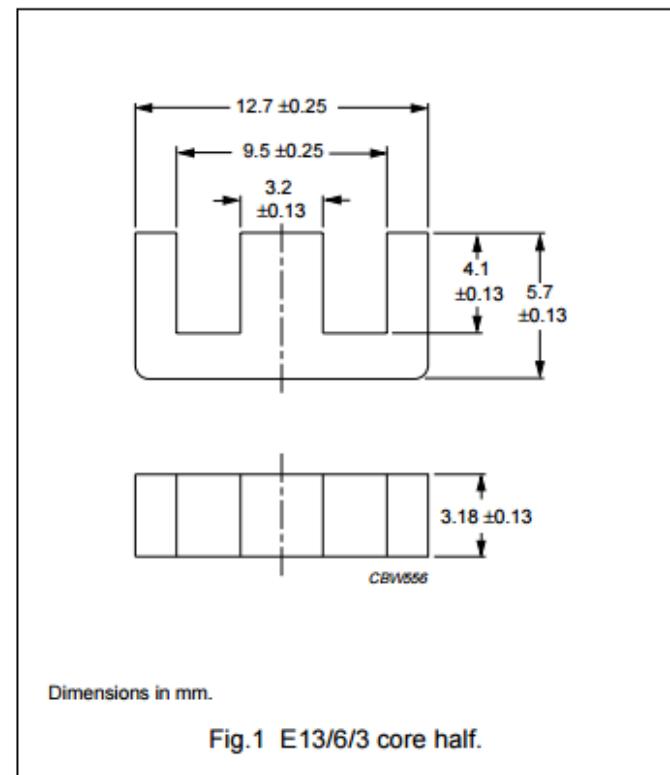
E cores and accessories

E13/6/3

CORE SETS

Effective core parameters

SYMBOL	PARAMETER	VALUE	UNIT
$\Sigma(I/A)$	core factor (C1)	2.74	mm^{-1}
V_e	effective volume	281	mm^3
l_e	effective length	27.8	mm
A_e	effective area	10.1	mm^2
A_{\min}	minimum area	10.1	mm^2
m	mass of core half	= 0.7	g



Core halves

A_L measured in combination with a non-gapped core half, clamping force for A_L measurements, 8 ± 4 N.

GRADE	A_L (nH)	μ_e	AIR GAP (μm)	TYPE NUMBER
3C90	$63 \pm 5\%$	= 138	= 250	E13/6/3-3C90-A63
	$100 \pm 8\%$	= 219	= 140	E13/6/3-3C90-A100
	$160 \pm 8\%$	= 350	= 75	E13/6/3-3C90-A160
	$250 \pm 20\%$	= 548	= 40	E13/6/3-3C90-A250
	$315 \pm 20\%$	= 690	= 30	E13/6/3-3C90-A315
	$730 \pm 25\%$	= 1590	= 0	E13/6/3-3C90
3C92 des	$540 \pm 25\%$	= 1180	= 0	E13/6/3-3C92
3C94	$730 \pm 25\%$	= 1590	= 0	E13/6/3-3C94
3C96 des	$660 \pm 25\%$	= 1440	= 0	E13/6/3-3C96

Diode 1:

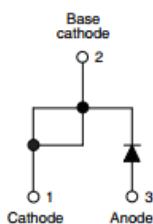


www.vishay.com

VS-HFA06TB120PbF, VS-HFA06TB120-N3

Vishay Semiconductors

HEXFRED®, Ultrafast Soft Recovery Diode, 6 A



TO-220AC

FEATURES

- Ultrafast and ultrasoft recovery
- Very low I_{RRM} and Q_{rr}
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



RoHS
COMPLIANT
HALOGEN
FREE
Antimony

BENEFITS

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION

VS-HFA06TB120... is a state of the art ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 V and 6 A continuous current, the VS-HFA06TB120... is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA06TB120... is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

$$R_{thJA} = 15.9^\circ\text{C}/\text{W}$$

PRODUCT SUMMARY	
Package	TO-220AC
$I_{F(AV)}$	6 A
V_R	1200 V
V_F at I_F	3.0 V
t_r typ.	26 ns
T_J max.	150 °C
Diode variation	Single die

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V_R		1200	V	
Maximum continuous forward current	I_F	$T_C = 100^\circ\text{C}$	6	A	
Single pulse forward current	I_{FSM}		80		
Maximum repetitive forward current	I_{FRM}		24	W	
Maximum power dissipation	P_D	$T_C = 25^\circ\text{C}$	62.5		
		$T_C = 100^\circ\text{C}$	25		
Operating junction and storage temperature range	T_J, T_{Stg}		-55 to +150	°C	

ELECTRICAL SPECIFICATIONS ($T_J = 25^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_R = 100 \mu\text{A}$	1200	-	-	V
Maximum forward voltage	V_{FM}	$I_F = 6.0 \text{ A}$	-	2.7	3.0	V
		$I_F = 12 \text{ A}$	-	3.5	3.9	
		$I_F = 6.0 \text{ A}, T_J = 125^\circ\text{C}$	-	2.4	2.8	
Maximum reverse leakage current	I_{RM}	$V_R = V_R$ rated	-	0.26	5.0	μA
		$T_J = 125^\circ\text{C}, V_R = 0.8 \times V_R$ rated	-	110	500	
Junction capacitance	C_T	$V_R = 200 \text{ V}$	-	9.0	14	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

Diode 2:

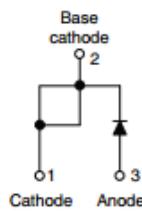
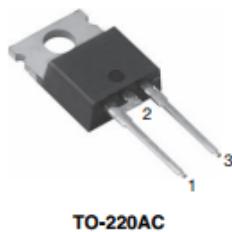


www.vishay.com

VS-10ETS...PbF Series, VS-10ETS...M3 Series

Vishay Semiconductors

High Voltage, Input Rectifier Diode, 10 A



FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- Designed and qualified according to JEDEC-JESD47
- Material categorization:
For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
Available

PRODUCT SUMMARY	
Package	TO-220AC
$I_{F(AV)}$	10 A
V_R	800 V to 1200 V
V_F at I_F	1.1 V
I_{FSM}	160 A
T_J max.	150 °C
Diode variation	Single die

APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	A

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Sinusoidal waveform	10	A
V_{RRM}		800/1200	V
I_{FSM}		160	A
V_F	10 A, $T_J = 25$ °C	1.1	V
T_J		- 40 to 150	°C

VOLTAGE RATINGS			
PART NUMBER	V_{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V_{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I_{RRM} AT 150 °C mA
VS-10ETS08PbF, VS-10ETS08-M3	800	900	
VS-10ETS12PbF, VS-10ETS12-M3	1200	1300	0.5

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	$T_C = 105$ °C, 180° conduction half sine wave	10	A
Maximum peak one cycle non-repetitive surge current	I_{FSM}	10 ms sine pulse, rated V_{RRM} applied	135	
		10 ms sine pulse, no voltage reapplied	160	
Maximum I^2t for fusing	I^2t	10 ms sine pulse, rated V_{RRM} applied	91	A^2s
		10 ms sine pulse, no voltage reapplied	130	
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$	$t = 0.1$ ms to 10 ms, no voltage reapplied	1300	A^2/s

$$R_{thJA} = 4.9 \text{ °C/W}$$